

LISTING OF AND AMENDMENTS TO CLAIMS:

1. (original) An active matrix substrate comprising:
 - a source electrode and a drain electrode which are provided on an insulating substrate and spaced apart from each other;
 - a semiconductor layer deposited on said source electrode and said drain electrode;
 - a gate insulating film deposited on said semiconductor layer;
 - a gate electrode deposited on said gate insulating film; and
 - a transparent conductive layer having first and second portions, the first portion being deposited on said gate electrode so as to be substantially the same pattern as that of said gate electrode, and the second portion including a portion deposited on a part of either said source electrode or said drain electrode.
2. (original) The active matrix substrate according to claim 1,
 - said active matrix substrate further comprising:
 - a data line connected to either said source electrode or said drain electrode,
 - wherein another portion of said gate insulating film is deposited on said data line.
3. (original) The active matrix substrate according to claim 1, wherein the second portion of said transparent conductive layer constitutes a pixel electrode so as to be

connected to either said source electrode or said drain electrode.

Claims 4, 5, 6 and 7 (canceled).

8. (Currently Amended) A display device comprising:

an active matrix substrate including:

a source electrode and a drain electrode which are provided on an insulating substrate and spaced apart from each other;

a semiconductor layer deposited on said source electrode and said drain electrode;

a gate insulating film deposited on said semiconductor layer;

a gate electrode deposited on said gate insulating film; and

a transparent conductive layer having first and second portions, the first portion being deposited on said gate electrode so as to be substantially the same pattern as that of said gate electrode, and the second portion including a portion deposited on a part of either said source electrode or said drain electrode

~~a thin film transistor structure formed on an insulating substrate;~~

~~a pixel electrode formed to be connected to either a source electrode or a drain electrode of said thin film transistor structure;~~

~~a data line formed to be connected to either the source electrode or the drain electrode of said thin film transistor structure; and~~

~~a gate line formed to be connected to a gate electrode of said thin film transistor structure,~~

~~wherein an upper surface of an upper electrode among said source, drain and gate electrodes of said thin film transistor structure is covered with an ITO film, and an upper surface of either said data line or said gate line is covered with a gate insulating film.~~

9. (presently amended) The display device according to claim 8, further comprising:

a liquid crystal layer filled by the use of said insulating substrate,

~~wherein upper surfaces of said upper electrode, said data line and said gate line which contact with said liquid crystal layer are covered with either said ITO film or said gate insulating film.~~

Claims 10, 11, 12, 13, 14, 15, 16, 17 and 18. (canceled)

19. (new) The display device of claim 8, further comprising:

a pixel electrode formed to be connected to one of the source electrode or the drain electrode of said thin film transistor structure;

a data line formed to be connected to the other of the source electrode or the drain electrode of said thin film transistor structure; and

a gate line formed to be connected to said gate electrode.

20. (new) The display device of claim 19, wherein:

an upper surface of an upper electrode among said source, drain and gate electrodes of said thin film transistor structure is covered with said transparent conductive layer, and an upper surface of either said data line or said gate line is covered with said gate insulating film.